



Product Service

**Technical Report No. 64.110.07.0792.01**

**Rev. 00**

**Dated July 30, 2007**

Client: G-Technologies Co., Ltd.  
Chaqiao Industrial District, Shiqi, Zhongshan City, 528404,  
Guangdong Province, P. R. China

Manufacturing place: Same as client

Test subject: Product: Control PCB for elevator  
Type: MCU

Test specification: IEC 60664-1:1992+A1:2000+A2:2002  
Manufacturer's requirement:  
For signal recording circuit, connecting with SDSL, CDSL RDSL,  
PSL and CSUP of terminal XM5, not including the low voltage part  
of the optocoupler and the circuits connecting with them,  
-Creepage distance between all lines on PCB not less than 4 mm  
-Clearance between all lines on PCB not less than 3 mm

Purpose of examination: • Test according to the test specification

Test result: The test subject was found to be in compliance with the test  
specification



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## 1 Description of the test subject

### 1.1 Function

Manufacturer's specification for intended use:

Control PCB for elevator

Model:	MCU
Rated Voltage Input:	230V~
Rated Frequency:	50Hz
Pollution Degree:	Not provided by client
Protection Against Liquids:	IP4X

## 2. Order

### 2.1 Date of Purchase Order

June 22, 2007

### 2.2 Receipt of Test Sample, Location

The test object was sent to Guangzhou on July 11, 2007.

### 2.3 Date of Testing

From July 11, 2007 to July 30, 2007

### 2.4 Location of Testing

The testing was done in Guangzhou, department ELS, per your order.

### 2.5 Points of Non-compliance or Exceptions of the Test Procedure

None

### 3. Test Results

The presented unit was found to be in compliance with the test specification.

#### 3.1 Positive Test Results

Electrical safety *IEC 60664-1:1992+A1:2000+A2:2002*  
 Manufacturer's requirement *Manufacturer's requirement:*

*For signal recording circuit, connecting with SDSL, CDSL RDSL, PSL and CSUP of terminal XM5, not including the low voltage part of the optocoupler and the circuits connecting with them,*

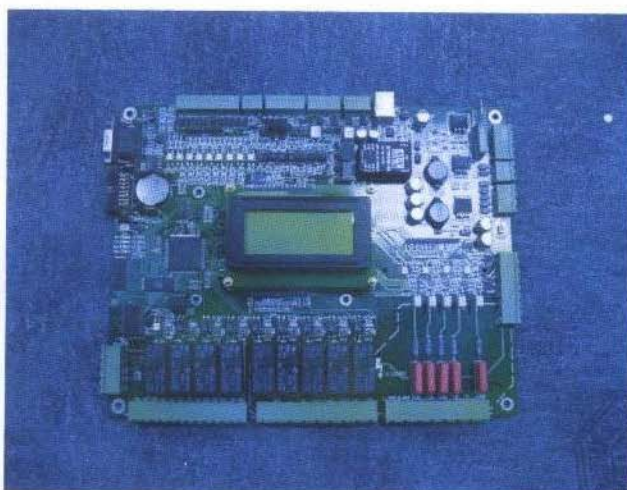
*-Creepage distance between all lines on PCB not less than 4 mm*

*-Clearance between all lines on PCB not less than 3 mm*

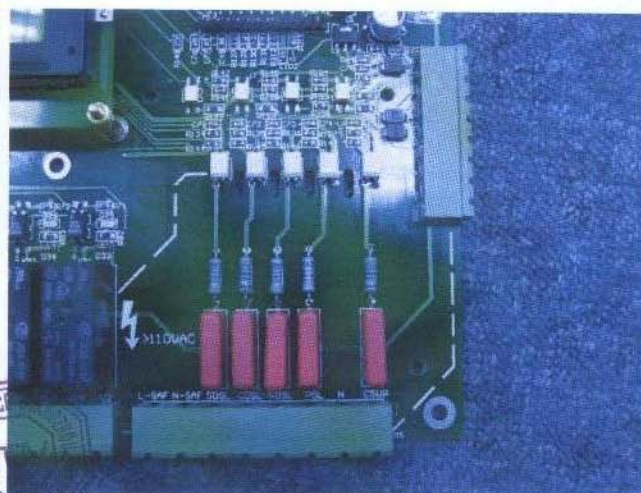
Test Position	Measured		limit	
	Clearance	Creepage distance	Clearance	Creepage distance
Between line of SDSL and CDSL	5,1 mm	5,1 mm	4,0 mm	3,0 mm
Between pins of terminal	6,4 mm	8,0 mm	4,0 mm	3,0 mm
Between soldering points of terminal	5,0 mm	5,0 mm	4,0 mm	3,0 mm

### 4 Remark

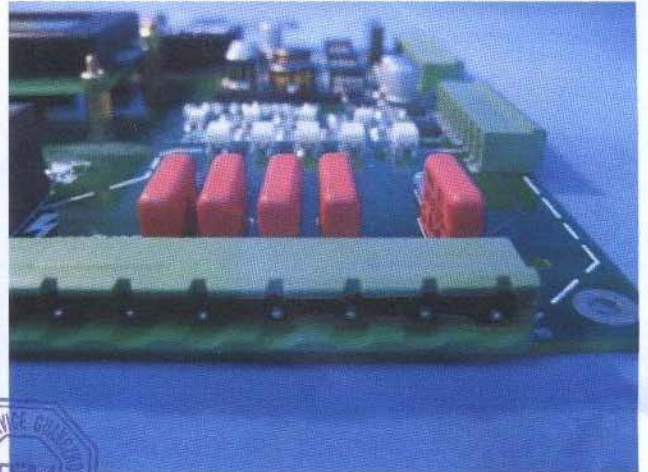
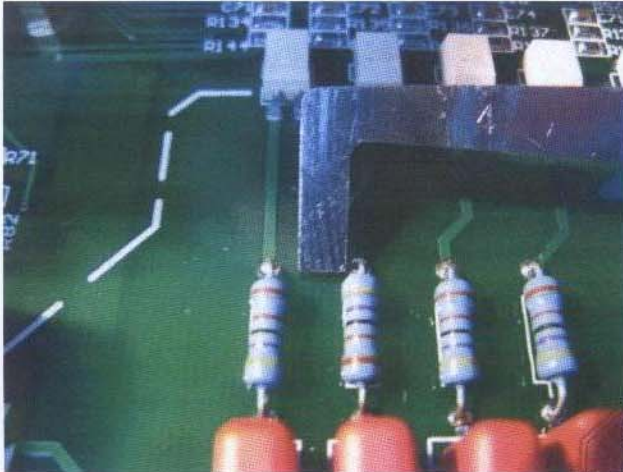
#### 4.1 Points of measurement



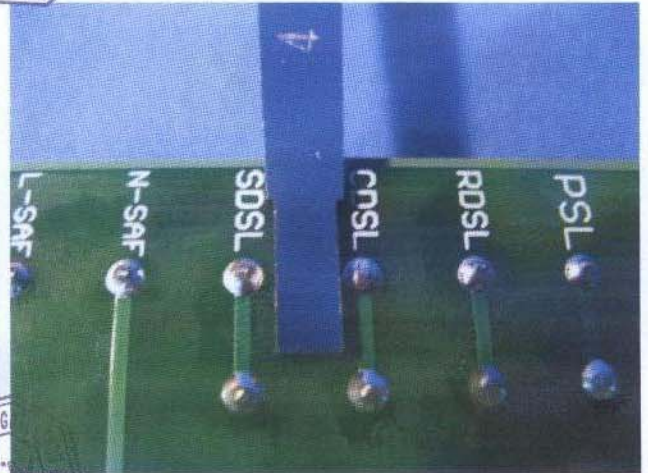
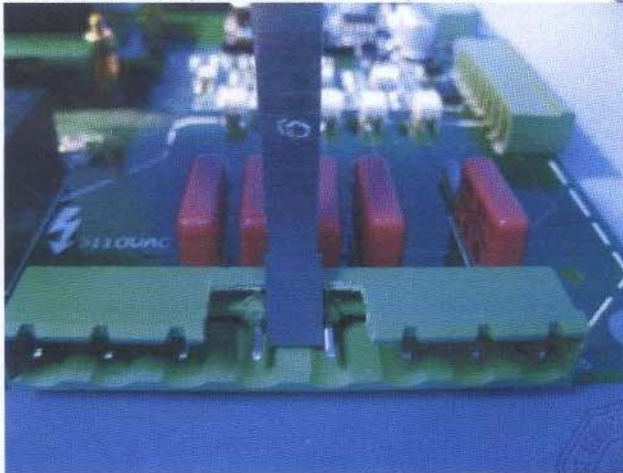
MCU board



Circuit that need to be measured



Creepage distance and clearance between Terminal XM5 lines on PCB, measured  $> 4,0$  mm



Creepage distance and clearance between pins of terminal XM5, measured  $> 4,0$  mm  
Creepage distance and clearance between welding of pins of terminal XM5, measured  $> 4,0$  mm

TÜV Product Service Ltd. Guangzhou Branch  
TÜV SÜD Group

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